

ERRATA

Chapter 2:

Table 2.11, Page 67

1. NAND gate missing circle at the output, please add.
2. XOR gate has extra circle at the output, please remove.

Laboratory 2.3, Page 90

1. Network 2, change label “StartSwitch_Left” to “StopSwitch_Left” .

Laboratory 2.4, Page 91

1. Second bullet change “precedinf” to “required”.

Chapter 3:

Below Figure 3.28, Page 110

1. Fifth line, (Q0.4) must change to (Q0.1).

Figure 3.29, Page 111

1. Change Tag name at input R to “RESET”.

Figure 3.42, Page 120

1. Change 50 to 10.

Figure 3.53, Page 126

1. In the “COUNTER1” block, change CTU to CTD.

Laboratory 3.2, Page 129

1. Seventh line, change 3 to 100.

Chapter 4:

OUT RANGE Instruction (Fig. 4.29), Page 147

1. Third line, change expression to VALUE < MIN or VAL > MAX,

Figure 4.30 Network 4, Page 149

1. Change “TIMER0_DN” from a normally open contact to a normally closed contact.

Example 4.4 Network 1 Description, Page 149

1. Change INPUT (I0.0) to INPUT (I0.1)

Figure 4.36, Page 154

1. Correct figure caption word “high” to “low”

Figure 4.46 Network 2, Page 160

1. Change PT input from 20S to 20H

Figure 4.47 Network 3, Page 161

1. Change “TIMER0-DN” from normally open contact to normally closed contact.

Chapter 8:

Laboratory Specifications, Page 329

1. First Bullet line 5, change (MD86) to (MD88) and (MD92) to (D96).
2. Third bullet, change MD 98 to MD 144.

Figure 8.47, Page 328

The scale must be corrected, each increment is 2. It should show 196, 198, 200, 202, and 204 values.

Chapter 9:

Figure 9.10, Page 340

1. Change MW26 to MD28.

Text above Figure 9.16, Page 343

1. Change Q1.0 to Q1.0/M42.5

Figure 9.33, Page 350

1. Change M42.1 to I0.4

Figure 9.35, Page 351

1. Change M24.3 to I0.5